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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/588,190	06/07/2000	Yoshiaki Shiota	067183/0186	8859
75	590 01/10/2003			
Foley & Lardner Washington Harbour 3000 K Street N W			EXAMINER	
			PUENTE, EMERSON C	
Suite 500 Washington, Do	C 20007-5109		ART UNIT	PAPER NUMBER
			2184	
			DATE MAILED: 01/10/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary		Application No.	Applicant(s)	<u></u>			
		09/588,190	SHIOTA, YOSHIAKI				
		Examiner	Art Unit				
		Emerson C Puente	2184				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHOI THE MA - Extension after SI - If the pe - If NO pe - Failure - Any rep	RTENED STATUTORY PERIOD FOR REPL'ALLING DATE OF THIS COMMUNICATION. Ons of time may be available under the provisions of 37 CFR 1.1 K (6) MONTHS from the mailing date of this communication. Friod for reply specified above is less than thirty (30) days, a reply- eriod for reply is specified above, the maximum statutory period to reply within the set or extended period for reply will, by statute by received by the Office later than three months after the mailing patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time y within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE.	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
1)🛛 🗆	Responsive to communication(s) filed on <u>07 c</u>	June 2000 .					
2a) <u></u> □	This action is FINAL . 2b)⊠ Th	is action is non-final.					
•	Since this application is in condition for allowance of Claims						
·	n of Claims	••					
	laim(s) is/are pending in the application Of the above claim(s) is/are withdraw						
_	· · · 	wn from consideration.					
	laim(s) is/are allowed.						
•	laim(s) <u>1-8</u> is/are rejected.		•				
	laim(s) is/are objected to.	a ala ati an an anci anno at					
Application	laim(s) are subject to restriction and/o Papers	r election requirement.					
9)[] Th	e specification is objected to by the Examine	г.					
10)□ Th	e drawing(s) filed on is/are: a)□ accep	oted or b)⊡ objected to by the Exar	miner.				
	Applicant may not request that any objection to the						
·	e proposed drawing correction filed on	,,	ved by the Examiner.				
	If approved, corrected drawings are required in rep	•					
	e oath or declaration is objected to by the Ex	aminer.					
<u> </u>	der 35 U.S.C. §§ 119 and 120						
·	cknowledgment is made of a claim for foreigr	n priority under 35 U.S.C. § 119(a)-(d) or (f).				
a)[_]	All b) Some * c) None of:						
•	Certified copies of the priority documents						
	Certified copies of the priority documents	· ·					
	Copies of the certified copies of the prior application from the International Bue the attached detailed Office action for a list	reau (PCT Rule 17.2(a)).	Ğ				
14) <u></u> Acl	knowledgment is made of a claim for domesti	c priority under 35 U.S.C. § 119(e	e) (to a provisional application)).			
	☐ The translation of the foreign language proknowledgment is made of a claim for domesti						
Attachment(s)						
2) Notice of	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) tion Disclosure Statement(s) (PTO-1449) Paper No(s) <u>3.</u>	5) Notice of Informal P	(PTO-413) Paper No(s) Patent Application (PTO-152)				

U.S. Patent and Trademark Office PTO-326 (Rev. 04-01) Application/Control Number: 09/588,190

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DETAILED ACTION

1. Claims 1-8 have been examined.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1, 3, 5, 6, and 7 are rejected under 35 U.S.C. §102(e) as being clearly anticipated by US Patent No. 6,202,090 of Simone.

In regards to claim 1, Simone discloses a fault management system for a switching equipment which included a circuit section and a processor for performing setting and control of said circuit section and transmits and receives data to and from a terminal equipment or the like, comprising:

a fault detection section for detecting a fault which occurs in said switching equipment. Simone states a reset occur for any one of a variety of software and hardware faults, indicating a fault detection section for detecting faults in the router or switching equipment (see column 4 lines 7-8).

a concentrated fault management section operable when said fault detection section detects an unrecoverable fault from continuously signaling a reset signal for resetting said processor and said section to said processor and said circuit section. Simone states a reset occur for any one of a variety of software and hardware faults (see column 4 lines 7-8).

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In regards to claim 3, Simone discloses a fault management system for a switching equipment wherein said concentrated fault management section is connected to and supervises a processor bus which interconnects said processor and said circuit section and continuously signals, when a fault occurs with said processor bus, a reset signal to said processor and said circuit section. Simone discloses an internal bus coupling interconnecting processor with said circuit section (see item 13 figure 1). Furthermore, Simone states a bus error calls a shutdown routine, which resets the processor and circuit section (see column 4 lines 10-13).

In regards to claims 5, 6, and 7, Simone discloses a fault management system for switching equipment wherein said concentrated fault management section sends a notification of occurrence of a fault to a central control section connected to an external console. Simone states the compressed core file can be accessed through a device (or external console) coupled to auxiliary port (see column 5 lines 34-37). The auxiliary port may constitute as a central control section because applicant discloses the central control section as being connected to external console and sending notification to the console when a fault occurs, which the auxiliary port does. The sending of the core compress file constitutes a notification of occurrence of a fault. If compress file was not received, no fault would of occurred.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. § 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 2, 4, and 8 are rejected under 35 U.S.C. § **103(a)** as being unpatentable over Simone in further view of Japanese Patent No. 02226432 of Nakamura.

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In regards to claim 2, Simone discloses all the claimed subject matter stated in claim 1, as previously stated. He further states a reset to occur for any one of a variety of software and hardware faults (see column 4 lines 7-8). Thus it is inherent when a fault has occurred, concentrated fault management section is notified of the unrecoverable fault in order to reset to occur.

However, Simone fails to disclose a fault management system for a switching equipment wherein said fault detection section detects whether or not supply of a clock signal of an oscillator which supplies the clock signal to said processor is interrupted.

Nakamura discloses a clock fault detection circuit, which detects abnormalities or interrupts in a clock supplied from a clock distribution circuit to processors through a clock signal (see abstract).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the teaching of Simone to include a clock fault detection section, which detects whether or not supply of a clock signal of an oscillator which supplies the clock signal to said processor is interrupted, as per teaching of Nakamura. A person of ordinary skill in the art would have been motivated to make the modification to Simone because Simone discloses resets occur for any one of a variety of software and hardware faults and having clock fault detection section, would indicate faults as a result of interrupts in clock signal to processor.

In regards to claim 4, Simone discloses a fault management system for a switching equipment wherein said concentrated fault management section is connected to and supervises a processor bus which interconnects said processor and said circuit section and continuously signals, when a fault occurs with said processor bus, a reset signal to said processor and said circuit section. Simone discloses an internal bus coupling interconnecting processor with said circuit section (see item 13 figure 1). Furthermore, Simone states a bus error calls a shutdown routine, which resets the processor and circuit section (see column 4 lines 10-13).

In regards to claim 8, Simone discloses a fault management system for switching equipment wherein said concentrated fault management section sends a notification of occurrence of a fault to a central control section connected to an external console.

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Simone states the compressed core file can be accessed through a device or external console coupled to auxiliary port (see column 5 lines 34-37). The auxiliary port may constitute as a central control section because applicant discloses the central control section as being connected to external console and sending notification to the console when a fault occurs, which the auxiliary port does. The sending of the core compress file constitutes a notification of occurrence of a fault. If compress file was not received, no fault would of occurred.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

See Form PTO-892.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Emerson Puente, whose telephone number is (703) 305-8012. The examiner can normally be reached on Monday-Friday from 8:00AM- 5:00PM, first Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, *Robert Beausoliel*, can be reached on (703) 305-9713 or via e-mail addressed to [robert.beausoliel@uspto.gov]. The fax number for the organization where this application or proceeding is assigned are (703) 746-7239 for regular communications and (703) 746-7238 for After Final communications.

Communications via Internet e-mail regarding this application, other than those under 35 U.S.C. 132 or which otherwise require a signature, may be used by the applicant and should be addressed to [emerson.puente@uspto.gov].

All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly signed express waiver of the confidentiality requirements of 35

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U.S.C. 122. This is more clearly set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89.

Any inquiry of a general nature or relating to the status of this application should be directed to the receptionist whose telephone number is (703) 305-3900.

Emerson Puente 1/3/03

ROBERT BEAUSOLIEL
SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2100